

Title: A PROCESS FOR MANUFACTURING TRANSISTORS HAVING SILICON/GERMANIUM CHANNEL REGIONS

Inventor(s): Yu

Appl. No.: 09/599,141

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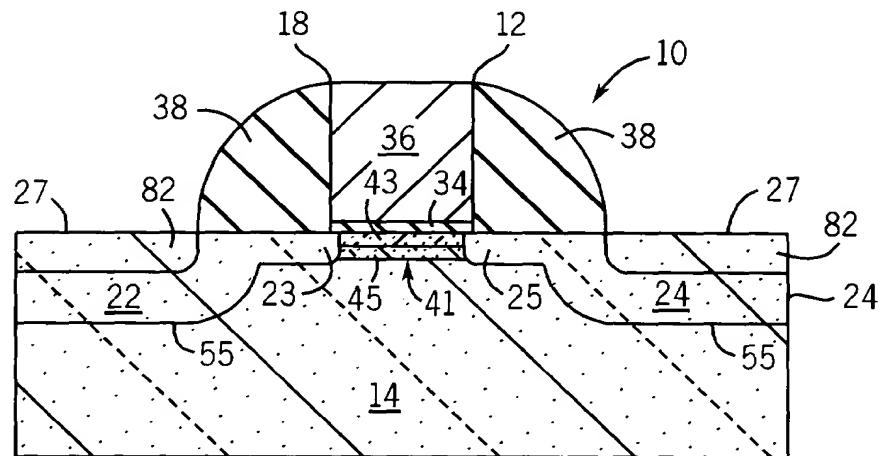


FIG. 1

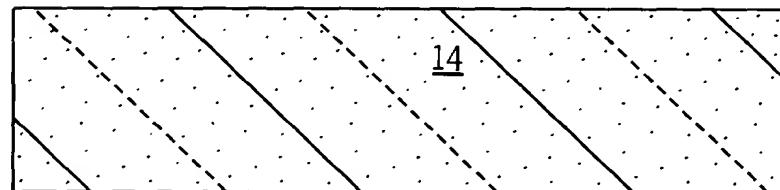


FIG. 2

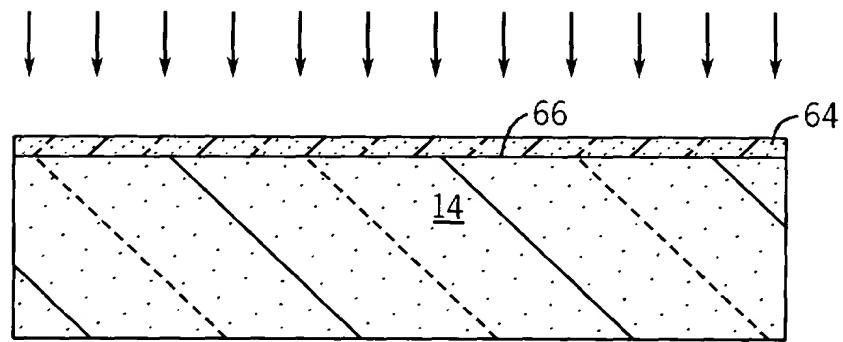


FIG. 3

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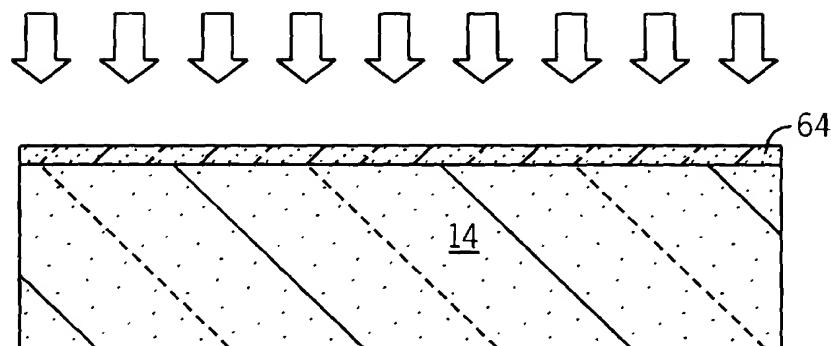


FIG. 4

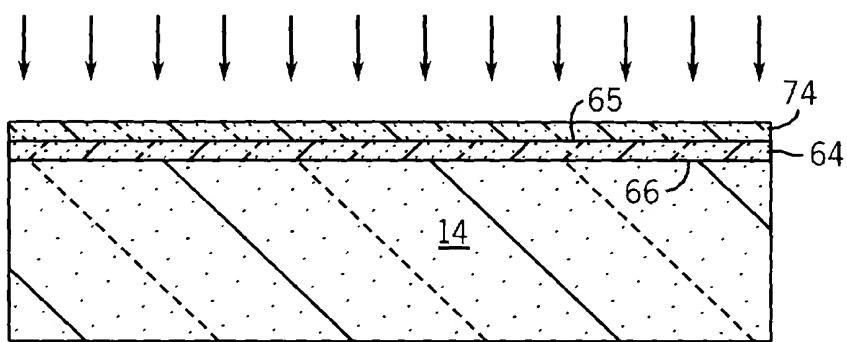


FIG. 5

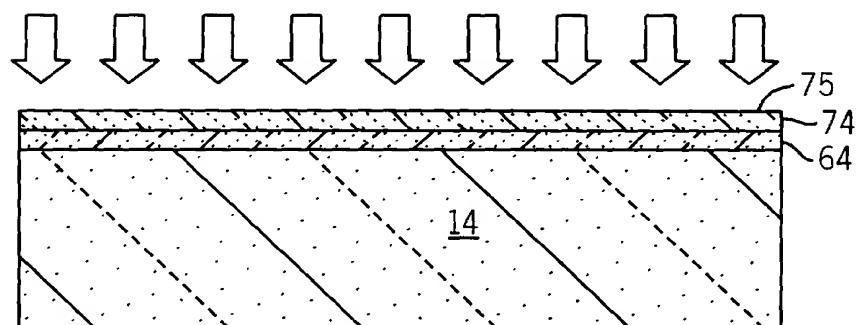


FIG. 6